

Electronic Acknowledgement Receipt

EFS ID:	7473956
Application Number:	10562235
International Application Number:	
Confirmation Number:	8539
Title of Invention:	Manufacturing Method Of Semiconductor Wafer Having a Trench Structure and Epitaxial Layer
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Filer Authorized By:	Sean Christophe Myers-Payne
Attorney Docket Number:	P35795
Receipt Date:	23-APR-2010
Filing Date:	31-OCT-2006
Time Stamp:	13:10:47
Application Type:	U.S. National Stage under 35 USC 371

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Amendment After Final	P35795_AMEND116.pdf	1524377 f40947aca25426a5d7d3fae77c318d6e1dbd4629	no	13

Warnings:

Information:

Total Files Size (in bytes):	1524377
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.